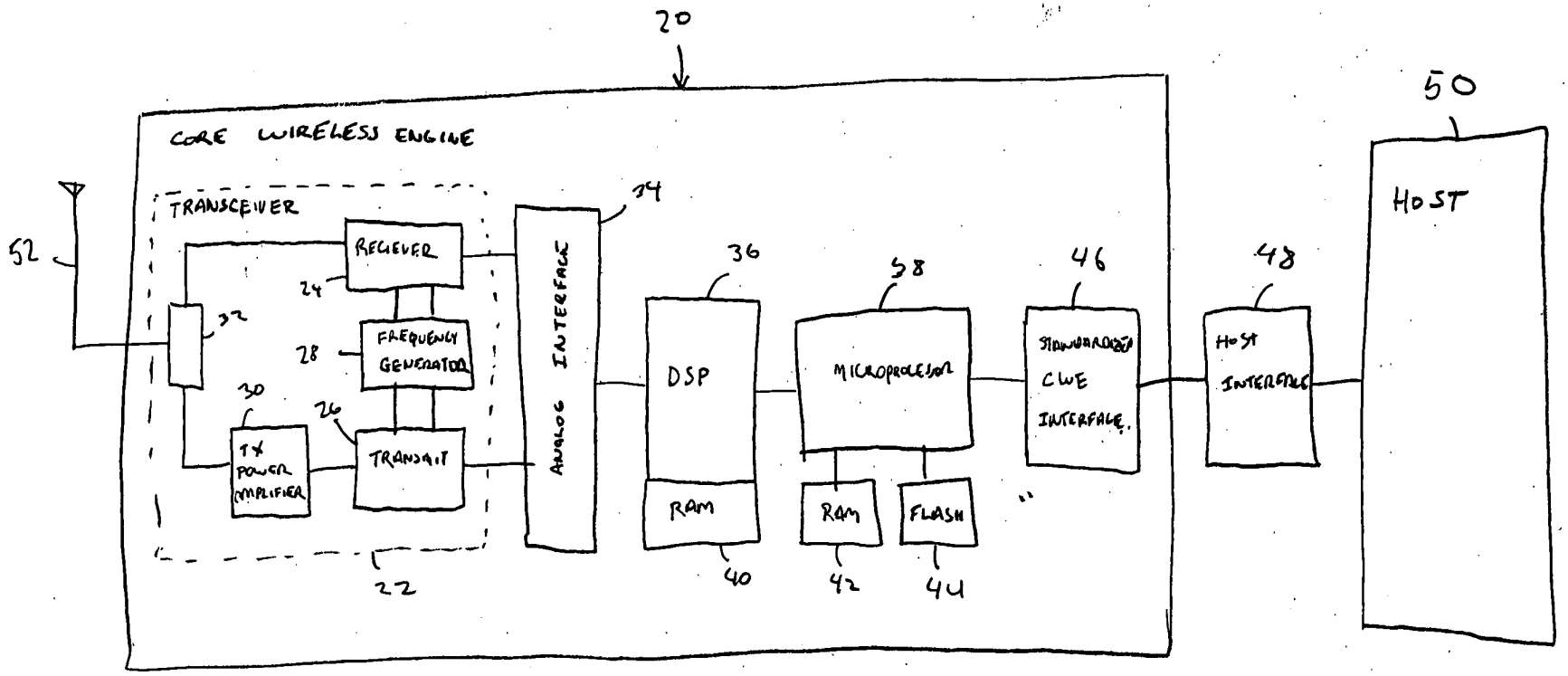

FIGURE 1

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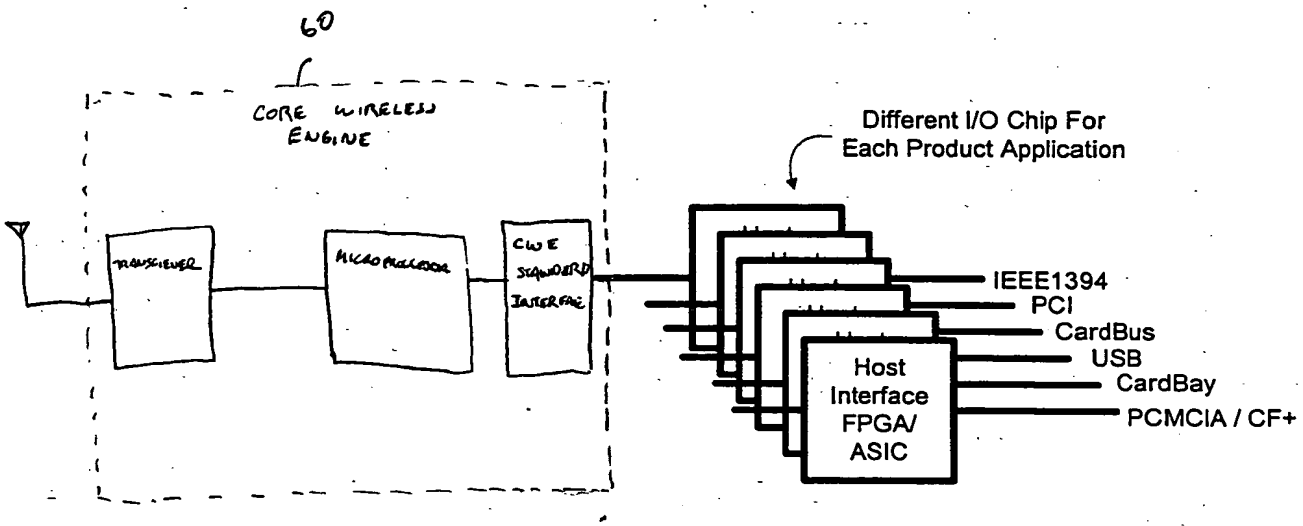
60
CORE WIRELESS ENGINE
TRANSCEIVER
MICROPROCESSOR
CWE STANDARD INTERFACE
Different I/O Chip For Each Product Application
Host Interface FPGA/ ASIC
IEEE1394
PCI
CardBus
USB
CardBay
PCMCIA / CF+

FIGURE 2

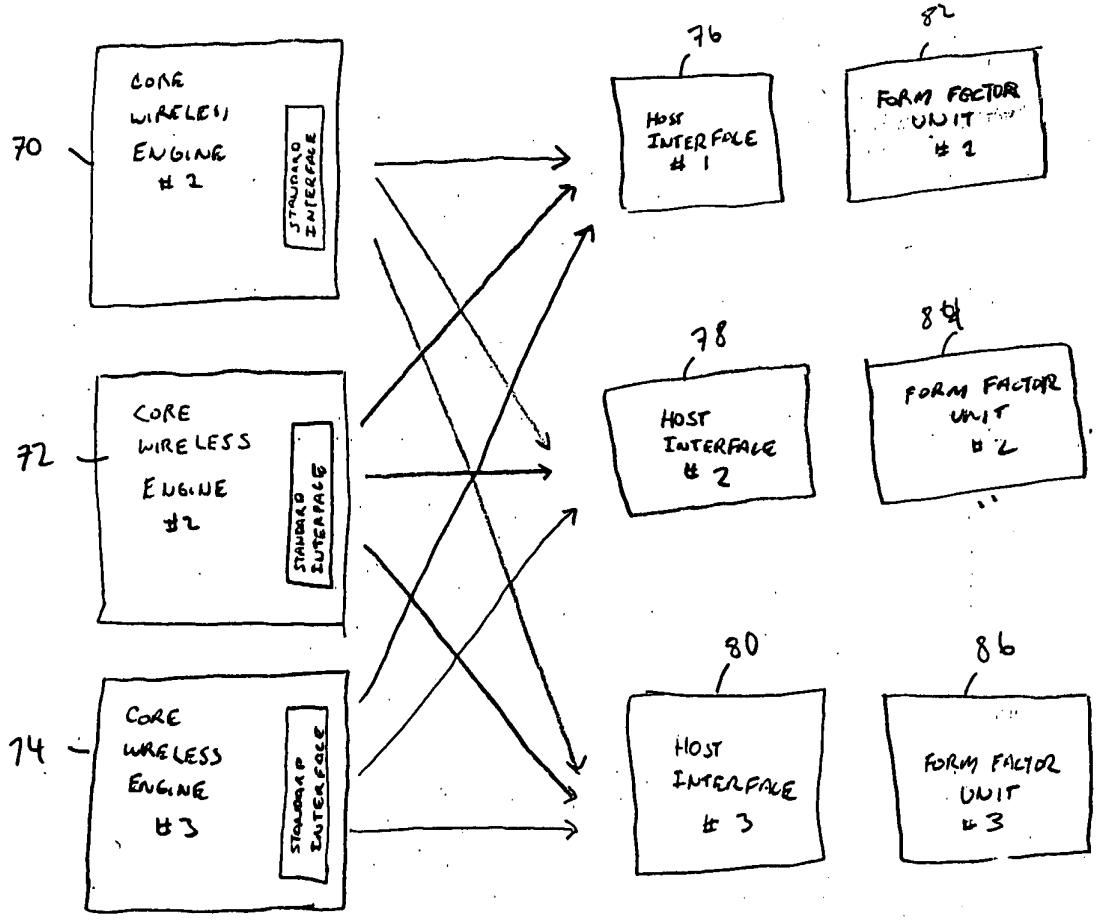
70
CORE WIRELESS ENGINE #2
STANDARD INTERFACE
72
CORE WIRELESS ENGINE #2
STANDARD INTERFACE
74
CORE WIRELESS ENGINE #3
STANDARD INTERFACE
76
HOST INTERFACE #1
78
HOST INTERFACE #2
80
HOST INTERFACE #3
82
FORM FACTOR UNIT #1
84
FORM FACTOR UNIT #2
86
FORM FACTOR UNIT #3

FIGURE 3

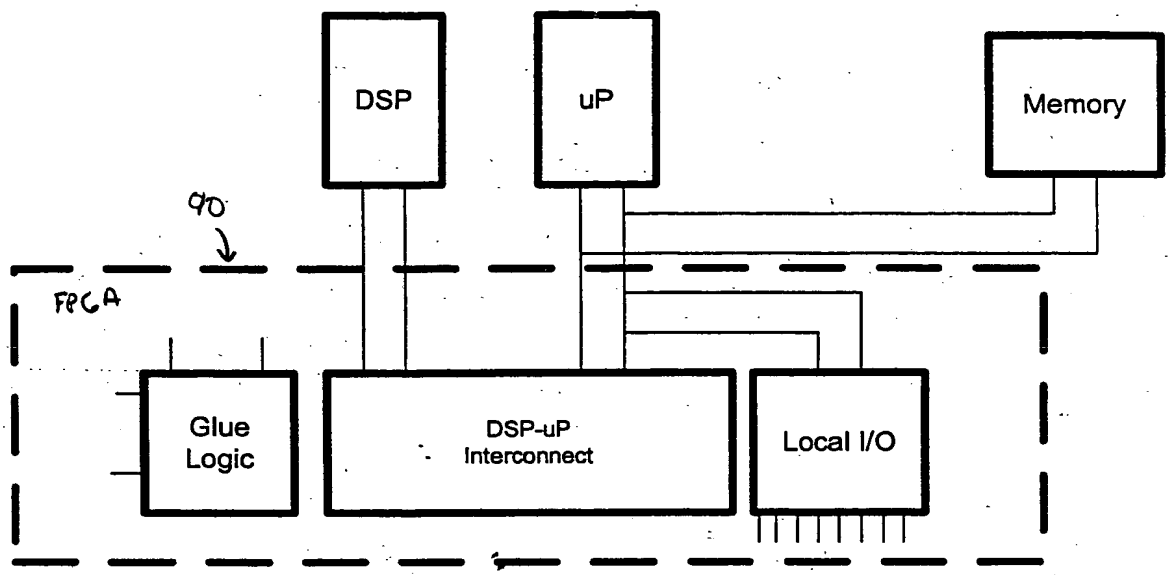

FIGURE 4A

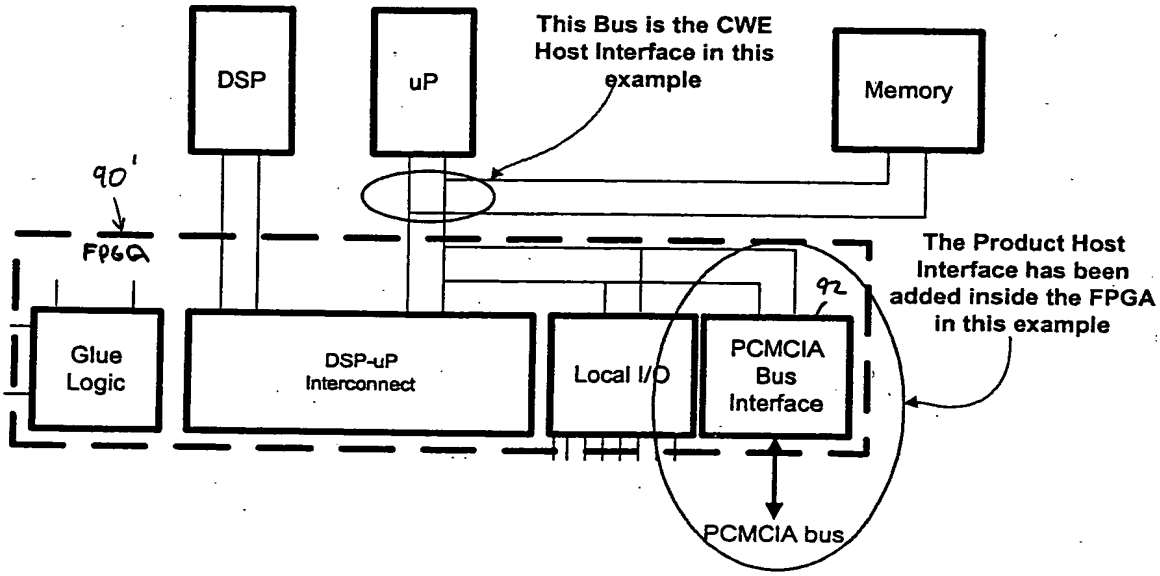

FIGURE 4B

Registers Visible to CWE MCU across CWE Host Interface

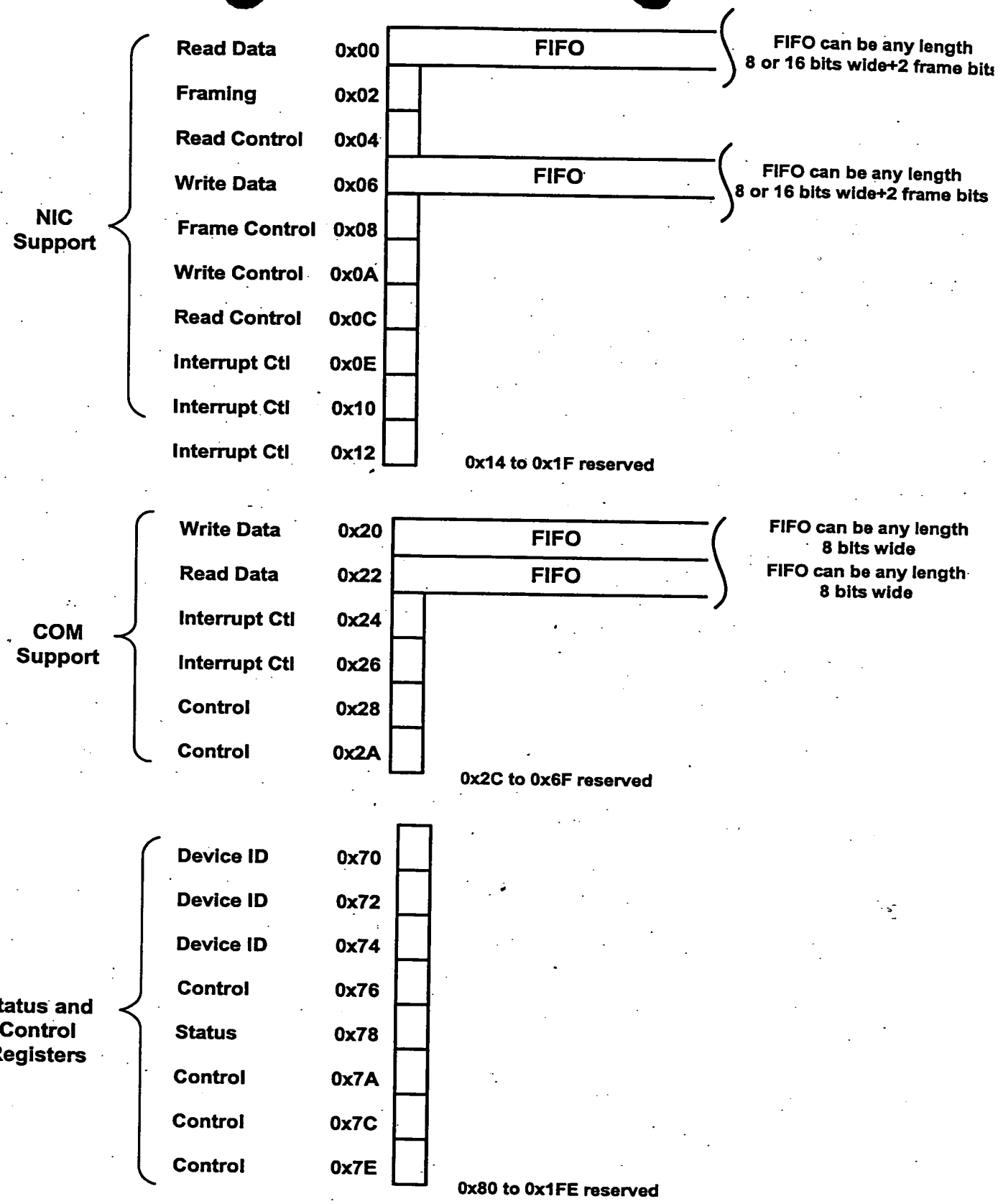

FIGURE 5

CWE Host Interface

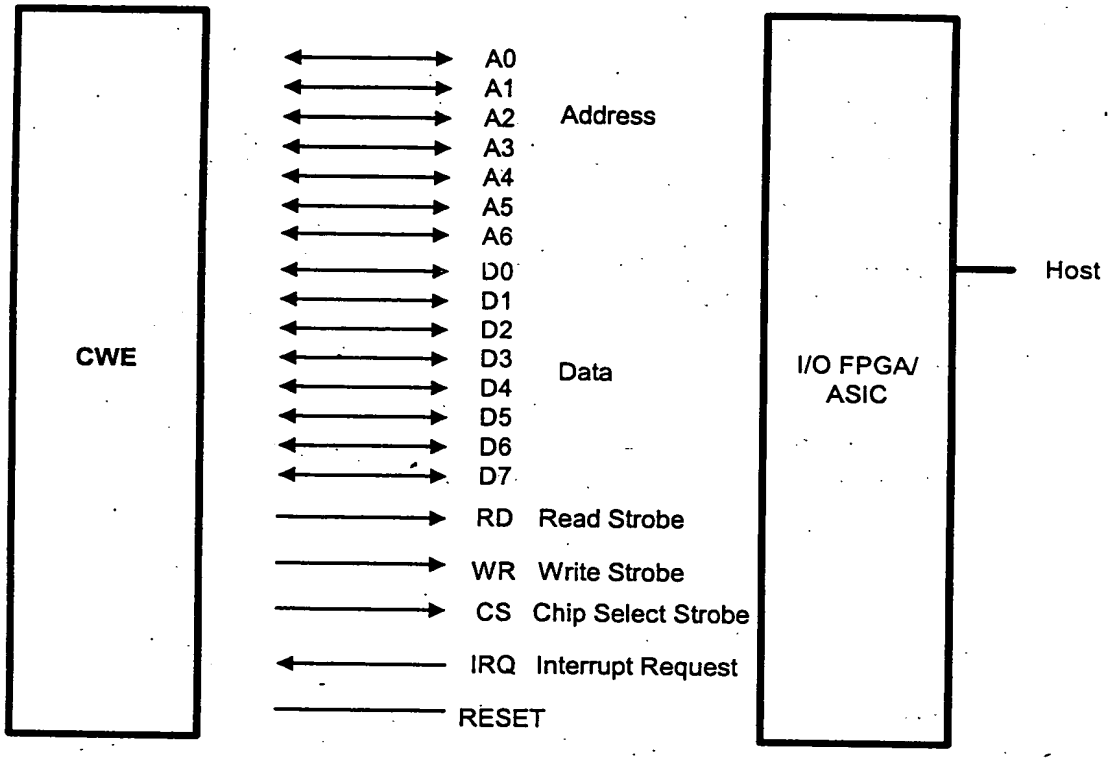

FIGURE 6

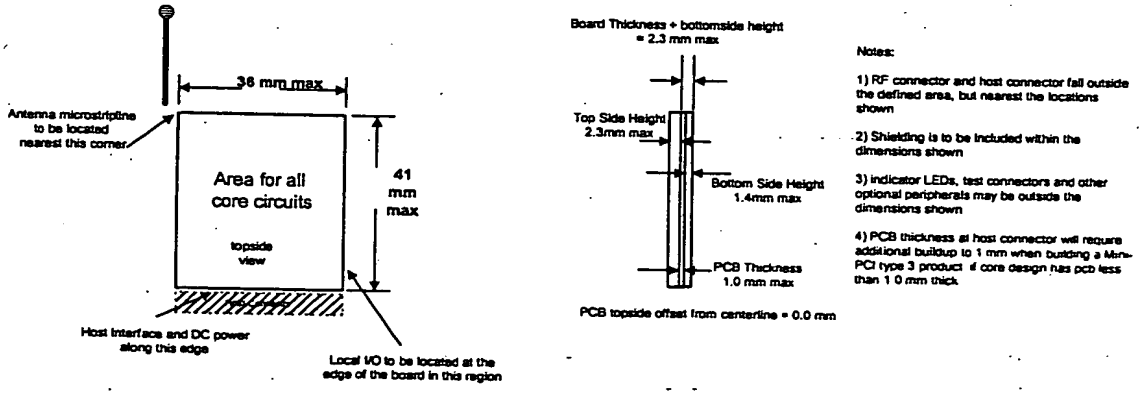

FIGURE 7

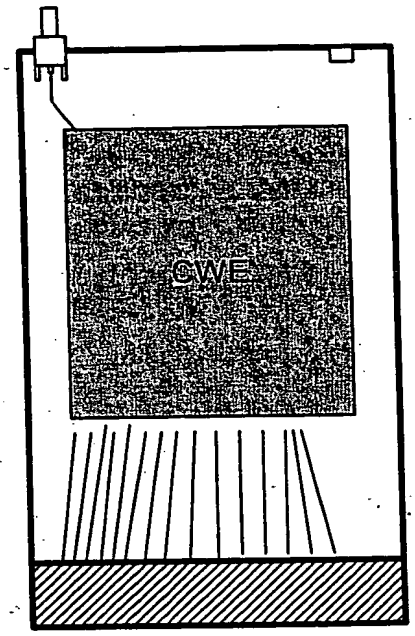

FIGURE 8A

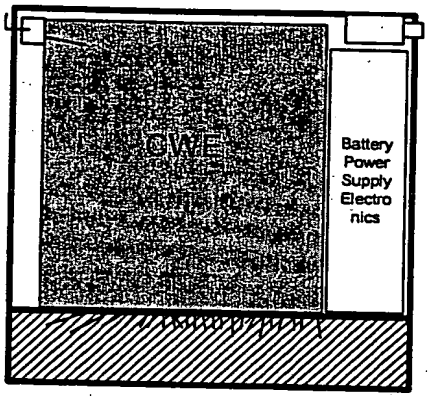

FIGURE 8B

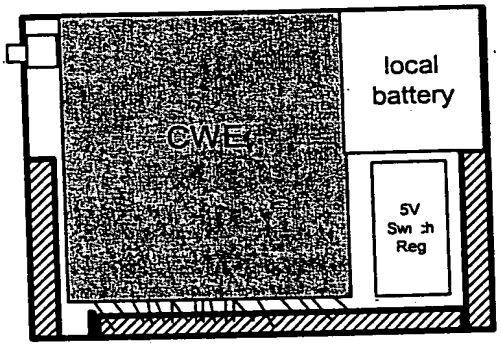

FIGURE 8C

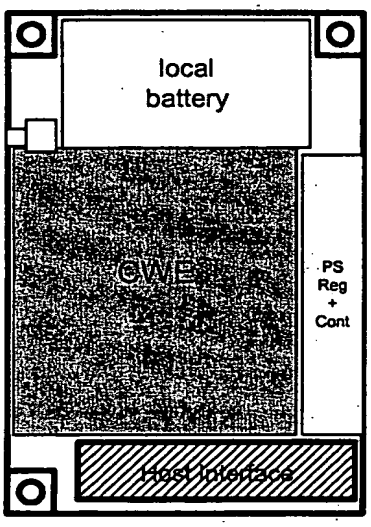

FIGURE 8D

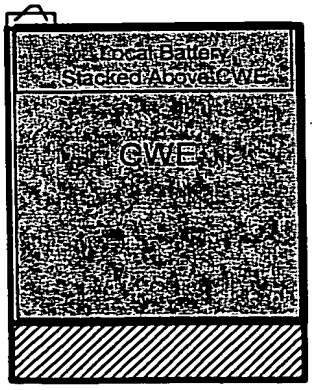

FIGURE 8E

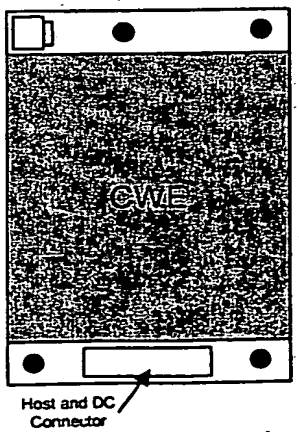

FIGURE 8F

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